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THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
MEARS ET AL.)
Serial No. **10/717,370**) Examiner R. Kunemund
Confirmation No. **6723**)
Filing Date: **NOVEMBER 19, 2003**) Art Unit: 1722
For: **METHOD FOR MAKING SEMICONDUCTOR DEVICE INCLUDING BAND-ENGINEERED SUPERLATTICE**) Attorney Docket:
) 62603_CON2
)

RESPONSE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Official Action of April 5, 2005,
please enter the amendments and remarks set out below.